

International **IR** Rectifier

INSULATED GATE BIPOLAR TRANSISTOR

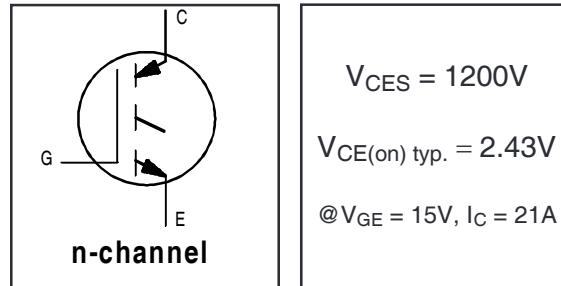
PD - 95187

IRG4PH40UPbF

Ultra Fast Speed IGBT

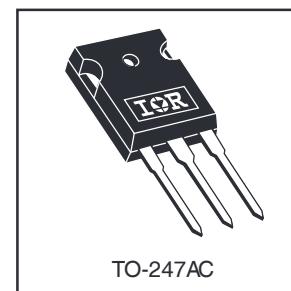
Features

- UltraFast: Optimized for high operating frequencies up to 40 kHz in hard switching, >200 kHz in resonant mode
- New IGBT design provides tighter parameter distribution and higher efficiency than previous generations
- Optimized for power conversion; SMPS, UPS and welding
- Industry standard TO-247AC package
- Lead-Free



Benefits

- Higher switching frequency capability than competitive IGBTs
- Highest efficiency available
- Much lower conduction losses than MOSFETs
- More efficient than short circuit rated IGBTs



Absolute Maximum Ratings

	Parameter	Max.	Units
V_{CES}	Collector-to-Emitter Breakdown Voltage	1200	V
$I_C @ T_C = 25^\circ C$	Continuous Collector Current	41	A
$I_C @ T_C = 100^\circ C$	Continuous Collector Current	21	
I_{CM}	Pulsed Collector Current ①	82	
I_{LM}	Clamped Inductive Load Current ②	82	
V_{GE}	Gate-to-Emitter Voltage	± 20	V
E_{ARV}	Reverse Voltage Avalanche Energy ③	270	mJ
$P_D @ T_C = 25^\circ C$	Maximum Power Dissipation	160	W
$P_D @ T_C = 100^\circ C$	Maximum Power Dissipation	65	
T_J	Operating Junction and	-55 to + 150	$^\circ C$
T_{STG}	Storage Temperature Range		
	Soldering Temperature, for 10 seconds	300 (0.063 in. (1.6mm) from case)	
	Mounting torque, 6-32 or M3 screw.	10 lbf-in (1.1N·m)	

Thermal Resistance

	Parameter	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case	—	0.77	$^\circ C/W$
$R_{\theta CS}$	Case-to-Sink, Flat, Greased Surface	0.24	—	
$R_{\theta JA}$	Junction-to-Ambient, typical socket mount	—	40	
Wt	Weight	6 (0.21)	—	g (oz)

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Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(\text{BR})\text{CES}}$	Collector-to-Emitter Breakdown Voltage	1200	—	—	V	$V_{\text{GE}} = 0\text{V}, I_C = 250\mu\text{A}$
$V_{(\text{BR})\text{ECS}}$	Emitter-to-Collector Breakdown Voltage ④	18	—	—	V	$V_{\text{GE}} = 0\text{V}, I_C = 1.0\text{A}$
$\Delta V_{(\text{BR})\text{CES}/\Delta T_J}$	Temperature Coeff. of Breakdown Voltage	—	0.43	—	V/ $^\circ\text{C}$	$V_{\text{GE}} = 0\text{V}, I_C = 1.0\text{mA}$
$V_{\text{CE}(\text{ON})}$	Collector-to-Emitter Saturation Voltage	—	2.43	3.1	V	$I_C = 21\text{A}$
		—	2.97	—		$I_C = 41\text{A}$
		—	2.47	—		$I_C = 21\text{A}, T_J = 150^\circ\text{C}$
		3.0	—	6.0		$V_{\text{CE}} = V_{\text{GE}}, I_C = 250\mu\text{A}$
$\Delta V_{\text{GE}(\text{th})/\Delta T_J}$	Temperature Coeff. of Threshold Voltage	—	-11	—	mV/ $^\circ\text{C}$	$V_{\text{CE}} = V_{\text{GE}}, I_C = 250\mu\text{A}$
g_{fe}	Forward Transconductance ⑤	16	24	—	S	$V_{\text{CE}} = 100\text{V}, I_C = 21\text{A}$
I_{CES}	Zero Gate Voltage Collector Current	—	—	250	μA	$V_{\text{GE}} = 0\text{V}, V_{\text{CE}} = 1200\text{V}$
		—	—	2.0		$V_{\text{GE}} = 0\text{V}, V_{\text{CE}} = 10\text{V}, T_J = 25^\circ\text{C}$
		—	—	5000		$V_{\text{GE}} = 0\text{V}, V_{\text{CE}} = 1200\text{V}, T_J = 150^\circ\text{C}$
I_{GES}	Gate-to-Emitter Leakage Current	—	—	± 100	nA	$V_{\text{GE}} = \pm 20\text{V}$

Switching Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
Q_g	Total Gate Charge (turn-on)	—	86	130	nC	$I_C = 21\text{A}$
Q_{ge}	Gate - Emitter Charge (turn-on)	—	13	20		$V_{\text{CC}} = 400\text{V}$
Q_{gc}	Gate - Collector Charge (turn-on)	—	29	44		See Fig. 8 $V_{\text{GE}} = 15\text{V}$
$t_{d(\text{on})}$	Turn-On Delay Time	—	24	—	ns	$T_J = 25^\circ\text{C}$ $I_C = 21\text{A}, V_{\text{CC}} = 960\text{V}$ $V_{\text{GE}} = 15\text{V}, R_G = 10\Omega$
t_r	Rise Time	—	24	—		
$t_{d(\text{off})}$	Turn-Off Delay Time	—	220	330		
t_f	Fall Time	—	180	270		
E_{on}	Turn-On Switching Loss	—	1.04	—	mJ	Energy losses include "tail" See Fig. 9, 10, 14
E_{off}	Turn-Off Switching Loss	—	3.40	—		
E_{ts}	Total Switching Loss	—	4.44	5.2		
$t_{d(\text{on})}$	Turn-On Delay Time	—	24	—	ns	$T_J = 150^\circ\text{C},$ $I_C = 21\text{A}, V_{\text{CC}} = 960\text{V}$ $V_{\text{GE}} = 15\text{V}, R_G = 10\Omega$
t_r	Rise Time	—	25	—		
$t_{d(\text{off})}$	Turn-Off Delay Time	—	310	—		
t_f	Fall Time	—	380	—		
E_{ts}	Total Switching Loss	—	7.39	—	mJ	Energy losses include "tail" See Fig. 11, 14
L_E	Internal Emitter Inductance	—	13	—	nH	Measured 5mm from package
C_{ies}	Input Capacitance	—	1800	—	pF	$V_{\text{GE}} = 0\text{V}$ $V_{\text{CC}} = 30\text{V}$ $f = 1.0\text{MHz}$
C_{oes}	Output Capacitance	—	120	—		
C_{res}	Reverse Transfer Capacitance	—	18	—		

Notes:

- ① Repetitive rating; $V_{\text{GE}} = 20\text{V}$, pulse width limited by max. junction temperature. (See fig. 13b)
- ② $V_{\text{CC}} = 80\%(V_{\text{CES}})$, $V_{\text{GE}} = 20\text{V}$, $L = 10\mu\text{H}$, $R_G = 10\Omega$, (See fig. 13a)
- ③ Repetitive rating; pulse width limited by maximum junction temperature.
- ④ Pulse width $\leq 80\mu\text{s}$; duty factor $\leq 0.1\%$.
- ⑤ Pulse width $5.0\mu\text{s}$, single shot.

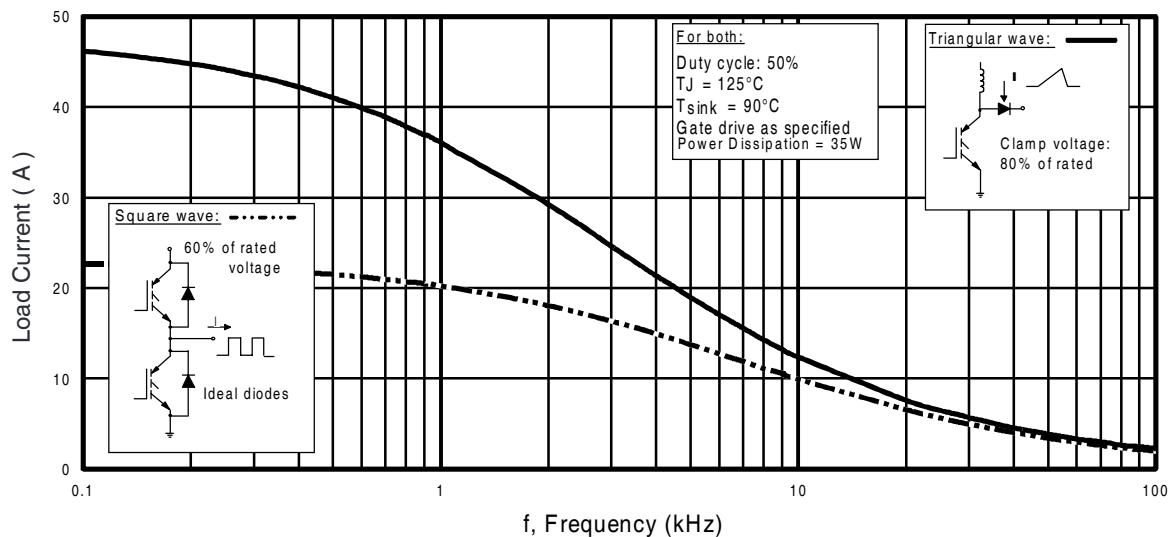


Fig. 1 - Typical Load Current vs. Frequency
 (Load Current = I_{RMS} of fundamental)

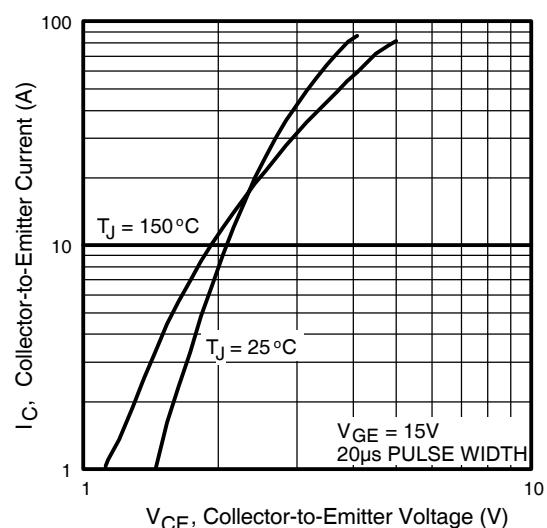


Fig. 2 - Typical Output Characteristics

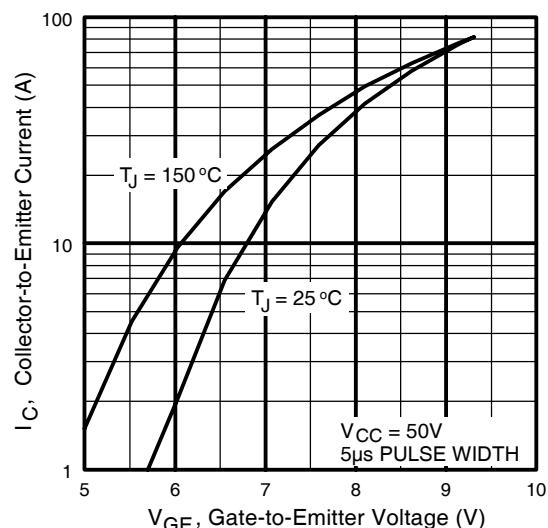


Fig. 3 - Typical Transfer Characteristics

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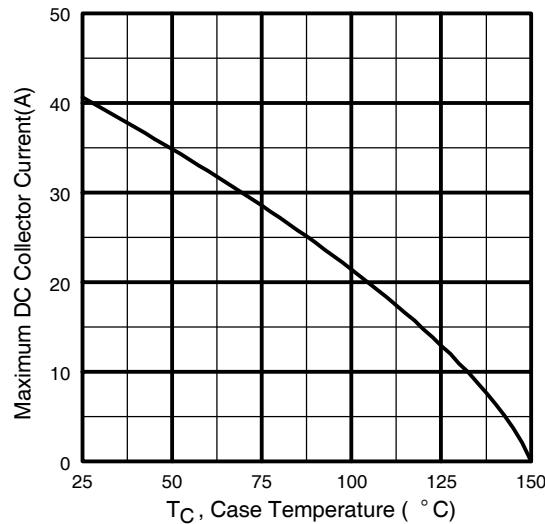


Fig. 4 - Maximum Collector Current vs. Case Temperature

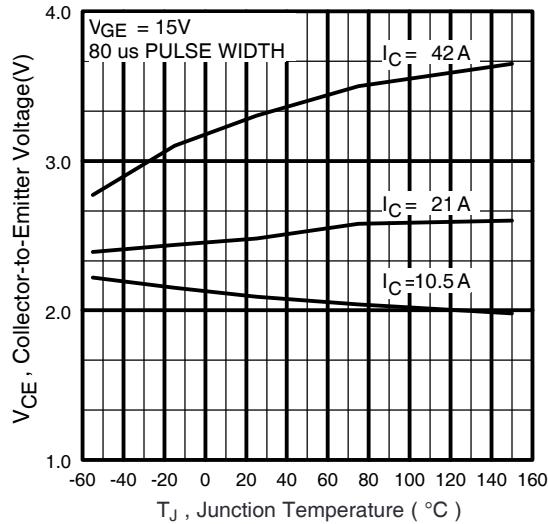


Fig. 5 - Typical Collector-to-Emitter Voltage vs. Junction Temperature

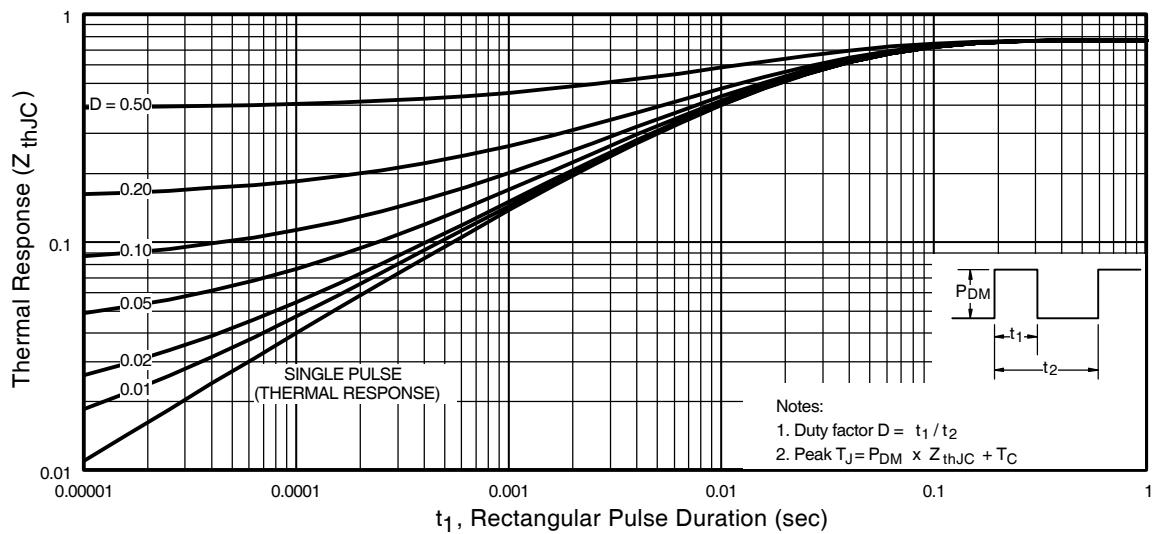
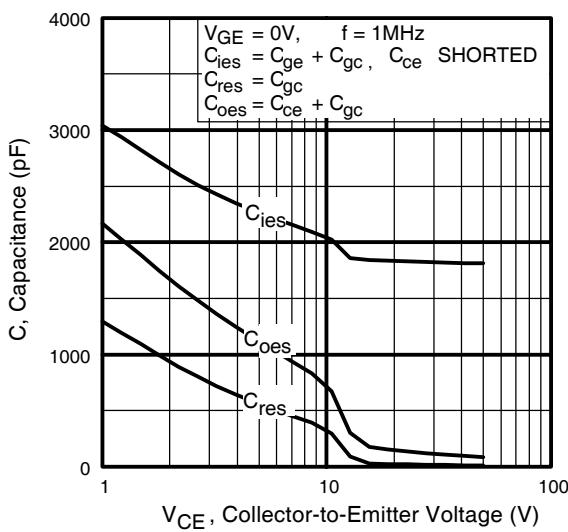
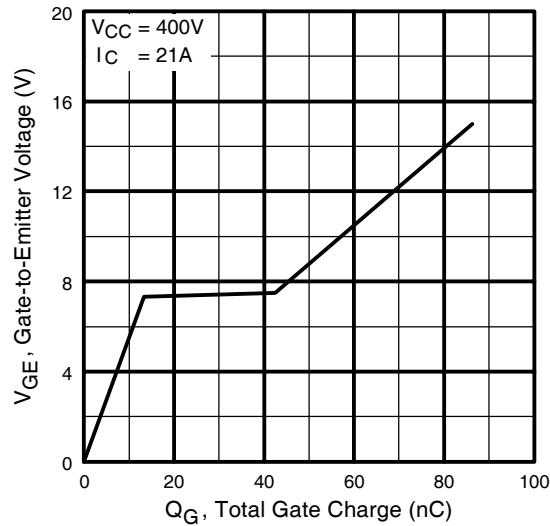


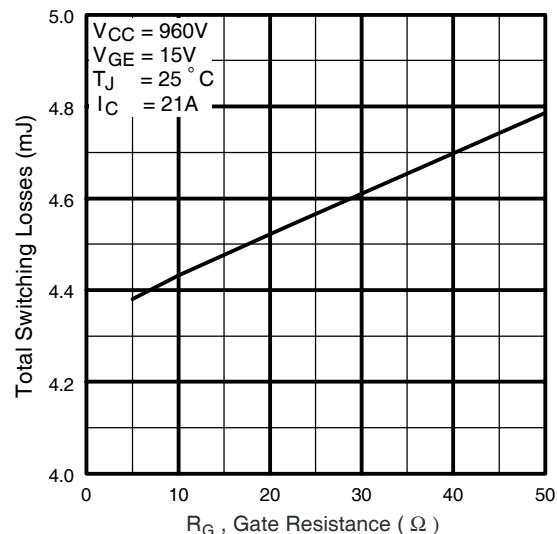
Fig. 6 - Maximum Effective Transient Thermal Impedance, Junction-to-Case



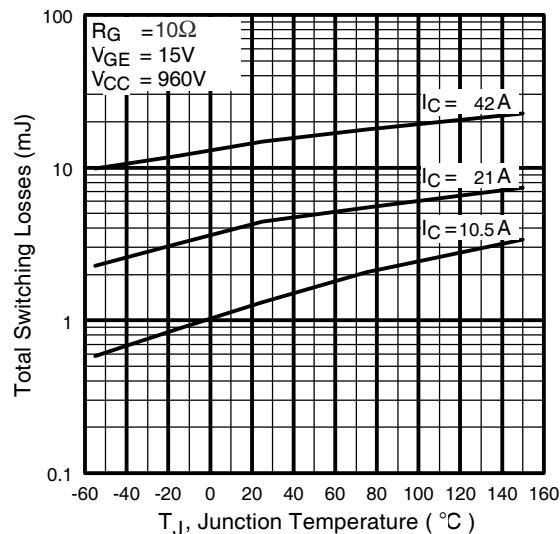
**Fig. 7 - Typical Capacitance vs.
Collector-to-Emitter Voltage**



**Fig. 8 - Typical Gate Charge vs.
Gate-to-Emitter Voltage**



**Fig. 9 - Typical Switching Losses vs. Gate
Resistance**



**Fig. 10 - Typical Switching Losses vs.
Junction Temperature**

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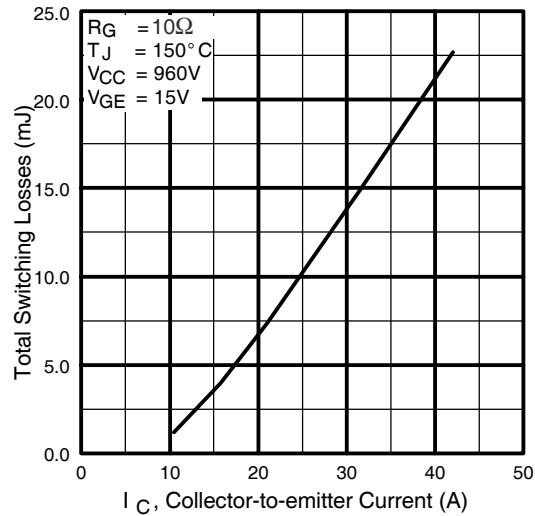


Fig. 11 - Typical Switching Losses vs.
Collector-to-Emitter Current

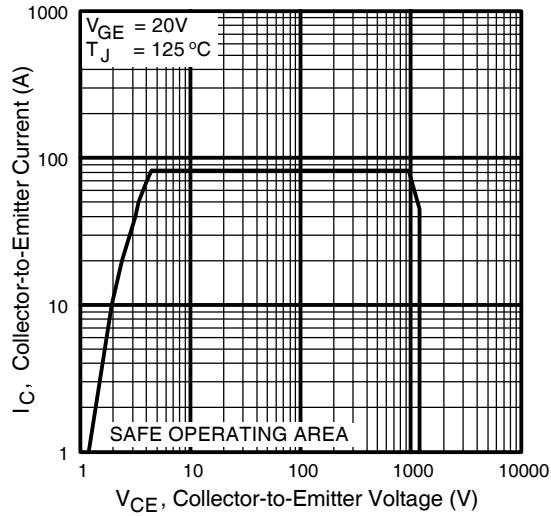


Fig. 12 - Turn-Off SOA

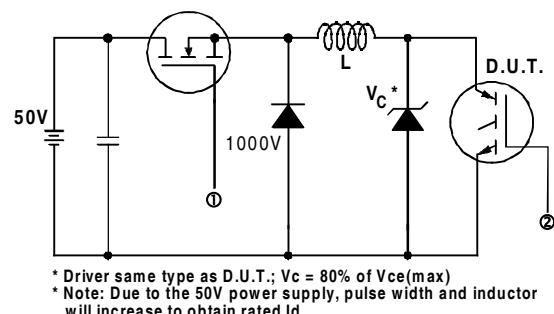


Fig. 13a - Clamped Inductive Load Test Circuit

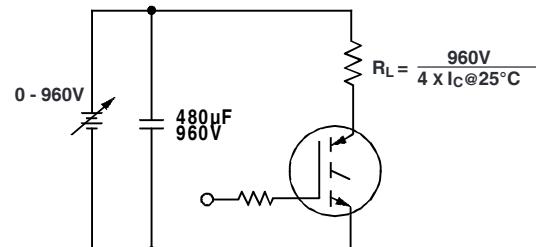


Fig. 13b - Pulsed Collector Current Test Circuit

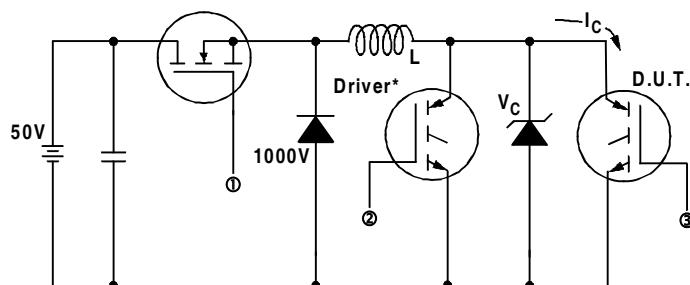


Fig. 14a - Switching Loss Test Circuit

* Driver same type as D.U.T., VC = 960V

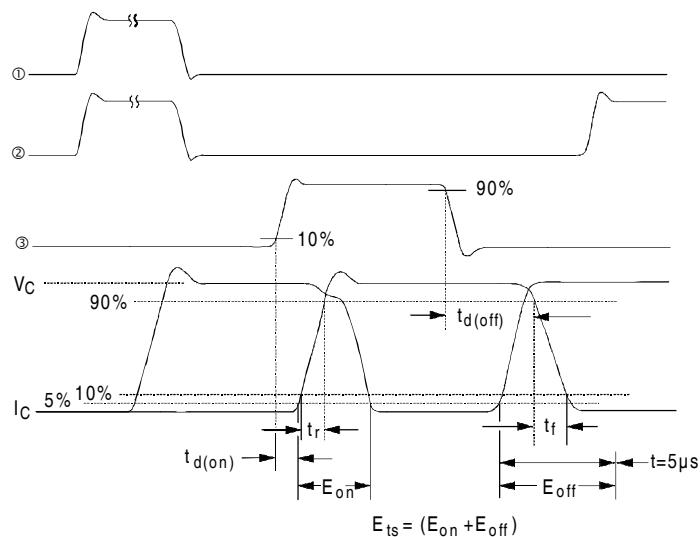


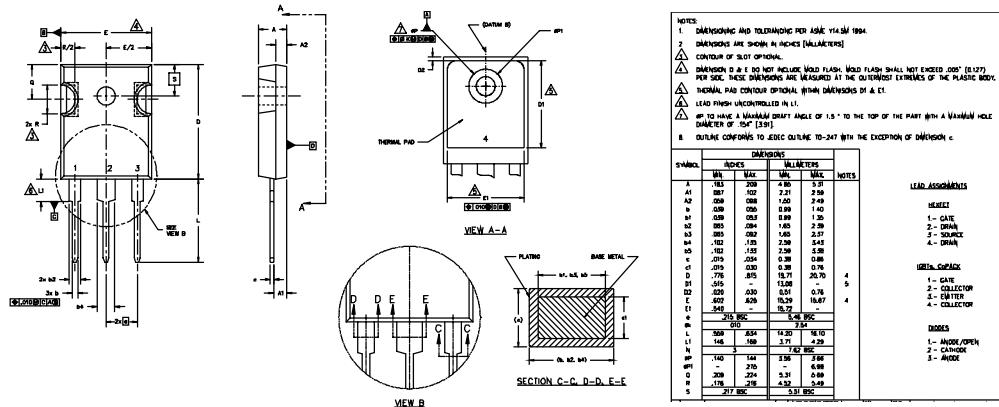
Fig. 14b - Switching Loss Waveforms

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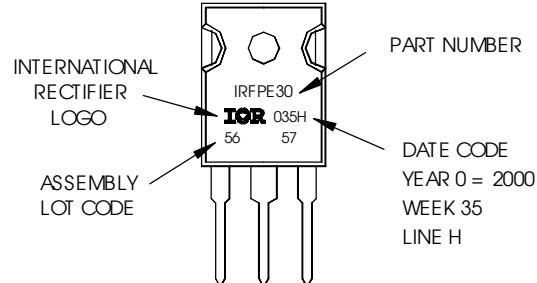
TO-247AC Package Outline

Dimensions are shown in millimeters (inches)



TO-247AC Part Marking Information

EXAMPLE: THIS IS AN IRFPE30
WITH ASSEMBLY
LOT CODE 5657
ASSEMBLED ON WW 35, 2000
IN THE ASSEMBLY LINE "H"
Note: "P" in assembly line
position indicates "Lead-Free"



Data and specifications subject to change without notice.

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Note: For the most current drawings please refer to the IR website at:
<http://www.irf.com/package/>